

INTERNATIONAL ELECTROTECHNICAL COMMISSION IEC Certification System for Explosive Atmospheres

for rules and details of the IECEx Scheme visit www.iecex.com

Certificate No.:	IECEx TRA 15.0003X	Page 1 of 5	Certificate history:
Status:	Current	Issue No: 2	Issue 1 (2017-06-08) Issue 0 (2015-08-28)
Date of Issue:	2021-08-21		
Applicant:	Nautitech Mining Systems Pty Ltd Unit 3, 9 Packard Avenue Castle Hill NSW 2154 Australia		
Equipment:	Lighting Interface Module (LIM)		
Optional accessory:			
Type of Protection:	Intrinsic safety "ib"		
Marking:	Ex ib I Mb -20C < Ta < +60C		
Approved for issue of Certification Body:	on behalf of the IECEx	Ajay Maira	
Position:		Certification Authority	
Signature: (for printed version)		Ajay Maine	
Date:		2021-08-21	
2. This certificate is no	schedule may only be reproduced in full. t transferable and remains the property of the issuing i enticity of this certificate may be verified by visiting w		
Certificate issued	d by:	_	_
Ex Testing and 1/30 Kenningtor Tomago NSW 23 Australia	Certification Pty Ltd n Drive 322		TESTING & CERTIFICATION



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Date of issue:	2021-08-21	Issue No: 2
Manufacturer:	Nautitech Mining Systems Pty Ltd Unit 3, 9 Packard Avenue Castle Hill NSW 2154 Australia	
Additional manufacturing locations:		
IEC Standard list belo found to comply with	ed as verification that a sample(s), representative of production, wa we and that the manufacturer's quality system, relating to the Ex pro the IECEx Quality system requirements.This certificate is granted s Operational Documents as amended	oducts covered by this certificate, was assessed and
STANDARDS : The equipment and a to comply with the foll	ny acceptable variations to it specified in the schedule of this certifi owing standards	cate and the identified documents, was found
IEC 60079-0:2011 Edition:6.0	Explosive atmospheres - Part 0: General requirements	
IEC 60079-11:2011 Edition:6.0	Explosive atmospheres - Part 11: Equipment protection by intrinsi	ic safety "i"
	This Certificate does not indicate compliance with safety and other than those expressly included in the Standar	
TEST & ASSESSME	NT REPORTS:	

A sample(s) of the equipment listed has successfully met the examination and test requirements as recorded in:

Test Report:

AU/TRA/ExTR15.0003/00

Quality Assessment Report:

AU/MSC/QAR21.0001/00



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EQUIPMENT:

Equipment and systems covered by this Certificate are as follows:

The Lighting Interface Module LIM is a purpose built apparatus that may be present in a configurable instrumented system built to achieve a safety and/or a control function.

The complete instrumented system may use several modules, where the modules are mechanically and electrically connected to each other using header-socket connections on the compatible sides that mate with each other, and the modules are fastened together to form one assembly.

For further details, see the Annexe.

SPECIFIC CONDITIONS OF USE: YES as shown below: See Annexe for details



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DETAILS OF CERTIFICATE CHANGES (for issues 1 and above) See Annexe for details



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Date of issue:

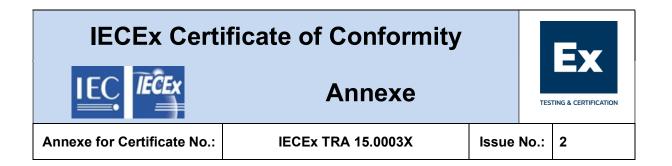
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Additional information:

Job 21105

Annex:

IECEx TRA 15.0003X-2 Annexe - final.pdf



Description:

The Lighting Interface Module (LIM) is a purpose built apparatus that may be present in a configurable instrumented system built to achieve a safety and/or a control function.

The complete instrumented system may use several modules, where the modules are mechanically and electrically connected to each other using header-socket connections on the compatible sides that mate with each other, and the modules are fastened together to form one assembly.

A typical configuration of the instrumented system may contain a power supply that connects to a suitably certified input source (usually alternator or other mains connected power source) and after its voltage and current limitation delivers power on a '4 Pin Power Rail' through all the modules, thus forming a backplane based connection system. This high power rail with Un = 20 V and In =11.9A is adequately segregated between the active and return lines, and also segregated from all other circuits, connection pins are duplicated and all the modules are fastened together to prevent sparking to be considered.

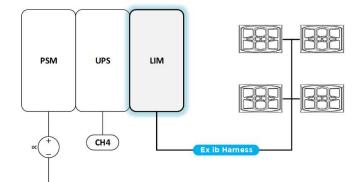
The LIM module receives power from the PSM Module on this high power rail (Un = 20 V) only when Zone 0 conditions are not present.

The LIM provides several intrinsically safe outputs:

- 9 separate 'LAMP' circuits, each with a Uo 20V lo 1.2A
- 1 combined 'GPIO' circuit that consists of 13 General Purpose Input Output lines with a combined Uo 20V Io 559mA

The LIM Module contains several internal printed circuit boards interconnected to each other. It is totally encapsulated.

A typical instrumented system example is depicted below.



Annexe



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Specific Conditions of Use pertaining to Issue 0 of this Certificate:

- a) The LIM must be installed with a compatible module on either side or end plates to form a complete system.
- b) The output plug and cabling is supplied by the equipment manufacturer to maintain adequate segregation of output circuits for installation.

c) The field socket JDRC-40 shall be fitted with ether a matching plug and cable or an end cap, which are at least IP54 rated.

d) The parameters provided below shall be taken into account in installation. Consult the manufacturer for assistance and advice.

External Connecto		1 = 1		
Description	Circuit	Pin	Function	Entity Parameters
		33	Power 1	
	1	23	Power 2	
		32	Common	
	2	31	Power 1	
		22	Power 2	
		21	Common	
		26	Power 1	
	3	17	Power 2	
		16	Common	
		34	Power 1	
	4	35	Power 2	Per Circuit:
		24	Common	110 - 20)/
Output Circuita	5	13	Power 1	- Uo = 20V - Io = 1.2A
Output Circuits		3	Power 2	- 10 - 1.2A - Co = 2uF
		2	Common	Lo = 100uH
	6	1	Power 1	- See Note
		12	Power 2	
		11	Common	
	7	7	Power 1	
		18	Power 2	
		6	Common	
	8 4	5	Power 1]
		4	Power 2]
		14	Common	7
	0	25	Power 1]
	9	15	Common	

External Connectors JDRC-40

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Description	Circuit	Pin	Function	Entity Parameters	
•		37	GPIO 1		
		36	GPIO 2		
		27	GPIO 3		
		38	GPIO 4		
	10	28	GPIO 5	Combined:	
		39	GPIO 6		
		10	29	GPIO 7	
		8	GPIO 8	lo = 559mA Co = 2uF	
		19	GPIO 9	Lo = 1mH	
		9	GPIO 10	See Note	
		20	GPIO 11		
	10	10	GPIO 12		
		30	GPIO 13		
		40	Common		

Note

The values of Lo and Co shall be reduced to 50% if both the following conditions are met:

a) The total Li of the external circuit (excluding the cable) ≥ 1% of the Lo value and

b) The total Ci of the external circuit (excluding the cable) \ge 1% of the Co value.

The reduced capacitance of the external circuit (including cable) shall not be greater than 1µF.

External Connectors JEF1

Description	Circuit	Pin	Function	Parameters
4 Pin Power Rail	Module supply	37	Ground	Un = 20V*
		36		I _n = 11.9A*
		35	Power	
		34		
-	-	Remaining pins	-	-
		are not		
		fitted/used in the		
		LIM		

* The Un 20V and In 11.9A are adequately segregated and ingress protected intrinsically safe voltage and current that is allowed in up to Zone 1 conditions but not allowed in Zone 0 conditions. The source of the 4pin power rail is a compatible module (e.g. PSM).

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Drawing list pertaining to Issue 0 of this Certificate:

Manufacturer's Documents

Title:	Drawing No.:	Pages	Rev. Level:	Date:
Part# 7680-1.0	ZUQPTY4FSNWN-57-	6	1	2015-07-24
LIM Main Left (All layers)	578			
PCB Artwork				
Part# 7682-1.0	ZUQPTY4FSNWN-57-	6	1	2015-07-24
LIM Main Right (All layers)	579			
PCB Artwork				
Part# 7684-1.0	ZUQPTY4FSNWN-57-	8	1	2015-07-24
LIM CPU Left (All layers)	580			
PCB Artwork				
Part# 7686-1.0	ZUQPTY4FSNWN-57-	6	1	2015-07-24
LIM Connector (All layers)	581			
PCB Artwork				
Part# 7688-1.0	ZUQPTY4FSNWN-57-	8	1.0	2015-08-10
BT Face Plate (All layers)	584			
PCB Artwork				
PART 2160-936-1	ZUQPTY4FSNWN-5-7-	1	1	2015-07-08
BTFP	577			
CERTIFICATION DETAIL				
Part# 7689-1.0	ZUQPTY4FSNWN-57-	1	1	2015-07-22
BTFP TOP SHEET	574			
Schematic				
Part# DS_EJ-1	ZUQPTY4FSNWN-57-	1	1	2014-06-03
Bluetooth 4.0 BLE	574			
Schematic				
PART 12035-1	ZUQPTY4FSNWN-51-	1	1	2015-07-06
LIM	575			
CERTIFICATION DETAIL				



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Title:	Drawing No.:	Pages	Rev. Level:	Date:
Part# 12035-1.0	ZUQPTY4FSNWN-57-	1 of 24	1	2015-02-20
Lighting Interface Module (LIM)	573			
Schematic				
Part# 7681-1.0	ZUQPTY4FSNWN-57-	2 of 24	2	2015-02-20
LIM MAIN LEFT COVERSHEET	573			
Schematic				
Part# 7683-1.0	ZUQPTY4FSNWN-57-	3 of 24	2	2015-02-20
LIM MAIN RIGHT COVERSHEET	573			
Schematic				
Part# 7685-1.0	ZUQPTY4FSNWN-57-	4 of 24	2	2015-02-20
LIM CPU LEFT COVERSHEET	573			
Schematic				
Part# 7686-1.0	ZUQPTY4FSNWN-57-	5 of 24	1	2015-07-07
LIM 40WAY FANOUT	573			
Schematic				
Part# 12035-1.0	ZUQPTY4FSNWN-57-	6 of 24	1	2015-03-12
Ex ib Active Current Limiter 1.2A	573			
Schematic				
Part# DS_HX-1.0	ZUQPTY4FSNWN-57-	7 of 24	1	2015-03-07
Active Current Limit 1.2A	573			
Schematic				
Part# 12035-1.0	ZUQPTY4FSNWN-57-	8 of 24	1	2015-07-15
LDO Power Limiter	573			
Schematic				
Part# 7681-1.0	ZUQPTY4FSNWN-57-	9 of 24	1	2015-07-07
Signal Barrier 1	573			
Schematic				
Part# 7683-1.0	ZUQPTY4FSNWN-57-	10 of 24	1	2015-07-07
Signal Barrier 2	573			
Schematic				



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Title:	Drawing No.:	Pages	Rev. Level:	Date:
Part# 7681-1.0	ZUQPTY4FSNWN-57-	11 of 24	2	2015-02-26
SAFE GPIO LEFT	573			
Schematic				
Part# 7682-1.0	ZUQPTY4FSNWN-57-	12 of 24	2	2015-02-26
SAFE GPIO RIGHT	573			
Schematic				
Part# 12035-1.0	ZUQPTY4FSNWN-57-	13 of 24	1	2015-02-27
GPIO ESD PROTECTION	573			
Schematic				
Part# 7683-1.0	ZUQPTY4FSNWN-57-	14 of 24	2	2014-03-04
SAFEPSU	573			
Schematic				
Part# 12035-1.0	ZUQPTY4FSNWN-57-	15 of 24	1	2015-07-07
DUAL OUTPUT SWITCH w/ OVERLOAD	573			
Schematic				
Part# 7683-1.0	ZUQPTY4FSNWN-57-	16 of 24	2	2014-03-04
PSU_1V2	573			
Schematic				
Part# 7683-1.0	ZUQPTY4FSNWN-57-	17 of 24	2	2014-03-04
PSU_3V3	573			
Schematic				
Part# 7685-1.0	ZUQPTY4FSNWN-57-	18 of 24	2	2015-03-04
CPU	573			
Schematic				
Part# 7681-1.0	ZUQPTY4FSNWN-57-	19 of 24	1	2015-07-07
EDGE CONNECTOR 1	573			
Schematic				
Part# 7683-1.0	ZUQPTY4FSNWN-57-	20 of 24	1	2015-07-07
EDGE CONNECTOR 2	573			
Schematic				



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Title:	Drawing No.:	Pages	Rev. Level:	Date:
Part# DS_BT-1.0	ZUQPTY4FSNWN-57-	21 of 24	1	2013-06-25
MEMORY_SPI_FLASH_4MB	573			
Schematic				
Part# DS_BR-1.1	ZUQPTY4FSNWN-57-	22 of 24	1	2013-05-29
SAFETY µP	573			
Schematic				
Part# DS_EF-1.0	ZUQPTY4FSNWN-57-	23 of 24	2	2013-07-22
CUBEX_BACKPLANE_LEFT	573			
Schematic				
Part# DS_BU-1.0	ZUQPTY4FSNWN-57-	24 of 24	2	2013-08-02
IO Expander 24-bit I2C	573			
Schematic				
PART 12035-1	ZUQPTY4FSNWN-57-	1	1	2015-07-06
LIM	576			
DATASHEET				

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Variations permitted by Issue 1 of this certificate:

- 1. The applicant and manufacturer have changed to NTMS.
- 2. Revised QAR reference to AU/ITA/QAR08.0004/09 to include this equipment in the scope of the audit of the manufacturer.
- 3. The manufacturer has submitted a complete set of drawings which have been retitled with their name. Where the drawing contained pictures showing the name of the earlier manufacturer, these have been edited to that extent. No other changes were made, and the revised drawing list is included below.
- 4. There are no changes in the parameters or conditions from the earlier issue of the certificate.

Specific Conditions of Use pertaining to Issue 1 of this certificate:

There are no changes to the conditions of use.

Drawings Associated with the Issue 1 of this Certificate:

Manufacturer's Documents

Title:	Drawing No.:	Pages	Rev. Level:	Date:
Part# 7680	ZUQPTY4FSNWN-57-	6	1.1	2015-07-24
LIM Main Left (All layers)	578			
PCB Artwork				
Part# 7682	ZUQPTY4FSNWN-57-	6	1.1	2015-08-10
LIM Main Right (All layers)	579			
PCB Artwork				
Part# 7684	ZUQPTY4FSNWN-57-	8	1.1	2015-07-24
LIM CPU Left (All layers)	580			
PCB Artwork				
Part# 7686	ZUQPTY4FSNWN-57-	6	1.1	2015-07-24
LIM Connector (All layers)	581			
PCB Artwork				
Part# 7688	ZUQPTY4FSNWN-57-	8	1.1	2015-08-10
BT Face Plate (All layers)	584			
PCB Artwork				
BTFP	DS_2160-936-1	1	2	2017-03-02
CERTIFICATION DETAIL				



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Title:	Drawing No.:	Pages	Rev. Level:	Date:
7689	ZUQPTY4FSNWN-57-	1 of 2	1.1	2015-07-22
BTFP TOP SHEET	574			
Schematic				
DS_EJ	ZUQPTY4FSNWN-57-	2 of 2	1.1	2014-03-06 ¹
Bluetooth 4.0 BLE	574			
Schematic				
12035	ZUQPTY4FSNWN-57-	1 of 24	1.1	2015-02-20
Lighting Interface Module (LIM)	573			
Schematic				
7681	ZUQPTY4FSNWN-57-	2 of 24	2.1	2015-02-20
LIM MAIN LEFT COVERSHEET	573			
Schematic				
7683	ZUQPTY4FSNWN-57-	3 of 24	2.1	2015-02-20
LIM MAIN RIGHT COVERSHEET	573			
Schematic				
7685	ZUQPTY4FSNWN-57-	4 of 24	2.1	2015-02-20
LIM CPU LEFT COVERSHEET	573			
Schematic				
7686	ZUQPTY4FSNWN-57-	5 of 24	1.1	2015-07-07
LIM 40WAY FANOUT	573			
Schematic				
12035	ZUQPTY4FSNWN-57-	6 of 24	1.1	2015-03-12
Ex ib Active Current Limiter 1.2A	573			
Schematic				
DS_HX	ZUQPTY4FSNWN-57-	7 of 24	1.1	2015-03-07
Active Current Limit 1.2A	573			
Schematic				
12035	ZUQPTY4FSNWN-57-	8 of 24	1.1	2015-07-15
LDO Power Limiter	573			
Schematic				



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Title:

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Issue No.: 2

Issue N	2	
-		
Rev. Level:		Date:
1.1		2015-07-07

Thuộ.	Drawing No	1 ages		Date.
7681	ZUQPTY4FSNWN-57-	9 of 24	1.1	2015-07-07
Signal Barrier 1	573			
Schematic				
7683	ZUQPTY4FSNWN-57-	10 of 24	1.1	2015-07-07
Signal Barrier 2	573			
Schematic				
7681	ZUQPTY4FSNWN-57-	11 of 24	2.1	2015-02-26
SAFE GPIO LEFT	573			
Schematic				
7682	ZUQPTY4FSNWN-57-	12 of 24	2.1	2015-02-26
SAFE GPIO RIGHT	573			
Schematic				
12035	ZUQPTY4FSNWN-57-	13 of 24	1.1	2015-02-27
GPIO ESD PROTECTION	573			
Schematic				
7683	ZUQPTY4FSNWN-57-	14 of 24	2.1	2014-03-04
SAFEPSU	573			
Schematic				
12035	ZUQPTY4FSNWN-57-	15 of 24	1.1	2015-07-07
DUAL OUTPUT SWITCH w/ OVERLOAD	573			
Schematic				
7683	ZUQPTY4FSNWN-57-	16 of 24	2.1	2014-03-04
PSU_1V2	573			
Schematic				
7683	ZUQPTY4FSNWN-57-	17 of 24	2.1	2014-03-04
PSU_3V3	573			
Schematic				
7685	ZUQPTY4FSNWN-57-	18 of 24	2.1	2015-03-04
CPU	573			
Schematic				



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Title:	Drawing No.:	Pages	Rev. Level:	Date:
7681	ZUQPTY4FSNWN-57-	19 of 24	1.1	2015-07-07
EDGE CONNECTOR 1	573			
Schematic				
7683	ZUQPTY4FSNWN-57-	20 of 24	1.1	2015-07-07
EDGE CONNECTOR 2	573			
Schematic				
DS_BT	ZUQPTY4FSNWN-57-	21 of 24	1.1	2013-06-25
MEMORY_SPI_FLASH_4MB	573			
Schematic				
DS_BR	ZUQPTY4FSNWN-57-	22 of 24	1.2	2013-05-29
SAFETY µP	573			
Schematic				
DS_EF	ZUQPTY4FSNWN-57-	23 of 24	2.1	2013-07-22
CUBEx_BACKPLANE_LEFT	573			
Schematic				
DS_BU-1.0	ZUQPTY4FSNWN-57-	24 of 24	2.1	2013-08-02
IO Expander 24-bit I2C	573			
Schematic				
LIM	12035-A	1 of 2	2	2017-02-15
CERTIFICATION DETAIL				
LIM	12035-A	2 of 2	2	2017-02-15
DATASHEET				

¹The date listed was incorrect and has been updated during issue 2 of this certificate.

Variations permitted by Issue 2 of this certificate:

• The manufacturer's Quality Assessment was changed from Ex Testing and Certification to another IECEx Certification Body, Mine Safety Technology Centre. QAR reference has been changed accordingly.

Specific Conditions of Use pertaining to Issue 2 of this certificate:

There are no changes to the conditions of use.

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Drawings Associated with the Issue 2 of this Certificate:

There are no drawings applicable to this issue of the certificate.